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	Application No.	Applicant(s) GHEEWALA, TUSHAR	
AA 48	10/806,752		
Notice of Allowability	Examiner	Art Unit	-
	Vibol Tan	2819	 -
The MAILING DATE of this communication appear All claims being allowable, PROSECUTION ON THE MERITS IS (herewith (or previously mailed), a Notice of Allowance (PTOL-85) of NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGOT (The Office or upon petition by the applicant. See 37 CFR 1.313	OR REMAINS) CLOSED in this apport of the substruction of the substruction of the subsect to the	plication. If not include n will be mailed in due o	d course. THIS
1. \boxtimes This communication is responsive to <u>the amendment filed</u> 8	<u>3/1/2005</u> .		
2. The allowed claim(s) is/are <u>1-4,6-14 and 19</u> .			
3. \boxtimes The drawings filed on <u>22 March 2004</u> are accepted by the E	Examiner.		
4.			
Attachment(s) 1. Notice of References Cited (PTO-892) 2. Notice of Draftperson's Patent Drawing Review (PTO-948) 3. Information Disclosure Statements (PTO-1449 or PTO/SB/08 Paper No./Mail Date 4. Examiner's Comment Regarding Requirement for Deposit of Biological Material	5. ☐ Notice of Informal F 6. ☐ Interview Summary Paper No./Mail Da 7. ☐ Examiner's Amendr 8. ☑ Examiner's Stateme 9. ☐ Other	(PTO-413), te ment/Comment	·

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Allowable Subject Matter

- 1. The following is an examiner's statement of reasons for allowance: in combination with other limitations of the claims, the cited prior arts fail to teach said programmable logic is operative in response to external control signals applied to said programmable logic and said control signals are operative to modify electrical functionality and connectivity of inputs and outputs connected to said programmable logic, as recited in amended claim 1; the cited prior arts also fail to teach said control logic is responsive to external control signals applied to said control logic and said control signals are operative to modify electrical functionality and connectivity of inputs and outputs connected to said control logic, as recited in amended claim 13; and the cited prior arts also fail to teach the step of switching signal lines via said programmable logic between first IC die and said second IC die, as recited in method claim 19.
- 2. Claims 1-4, 6-14 and 19 are now in condition for allowance.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Vibol Tan whose telephone number is (571) 272-1811. The examiner can normally be reached on Monday-Friday (7:00 AM-4:30 PM).

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Pascal can be reached on (571) 272-1769. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

VIBOL TAN
PRIMARY EXAMINER